

# Chance Reimer

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<b>Education</b>	<b>University of Central Florida</b> , Orlando, Florida Bachelor of Science in Computer Engineering and Electrical Engineering GPA: 3.67 Spring 2020		
	<b>Brevard Community College</b> , Cocoa, Florida Associates in Arts Degree GPA: 3.95 Spring 2013		
<i>Related Courses</i>	Digital Systems Electromagnetics	Image Processing Electronics I, II	Physics I, II, III Computer Organization
<b>Projects</b>	<b>Air Quality Sensor</b> Dr. Haofei Yu 09/2018 - Present <ul style="list-style-type: none"><li>Assist with development of software to read data from a sensor using an STM32, and store that data on an SD card</li><li>Development of an IOT web application to communicate with the Particle Photon by shifting out data stored on SD card</li><li>Adjusting application to communicate with multiple boards in service</li></ul>		
	<b>M-TADS/PNVS Support</b> Lockheed Martin Apache Program 05/2017 - 4/2018 <ul style="list-style-type: none"><li>Design of NI TestStand software used in diagnostics of analog and digital circuitry, allowing for decreased repair times and identification of problem components</li><li>DIT-MCO coding for verification of wire harnesses. Software greatly decreases troubleshooting time for modified harnesses. Assisted in design of harness adapters for DIT-MCO machine with a mechanical engineer</li><li>Analysis and manipulation of C and Verilog code, used for troubleshooting of digital components including ADCs, DACs, and FPGAs</li></ul>		
<b>Work Experience</b>	<b>Undergraduate Research Assistant</b> UCF Center for Microgravity Research 09/2018 - Present <ul style="list-style-type: none"><li>Assist with the development of STM32 microcontroller code for a CubeSat, using Atollic Studio</li><li>Soldering components onto, and developing PCB boards</li><li>Trouble-shooting electrical shorts</li></ul>		
	<b>Siemens Intern</b> SIE Orlando - Plant Layout Electrical Engineering Team 05/2018 - Present <ul style="list-style-type: none"><li>Estimation of interconnecting electrical quantities based on plant layout general diagrams</li><li>Development of electrical specifications to meet contract requirements</li><li>Support in evaluation of contractor electrical claims</li></ul>		
	<b>UCF College Work Experience Participant</b> CWEP participant for Sustainment Engineering Team – Apache 05/2017 - 4/2018 <ul style="list-style-type: none"><li>Design of test software</li><li>Integration of test software and hardware for the M-TADS/PNVS systems</li><li>Troubleshooting and optimization</li></ul>		
<b>Skills</b>	<b>Programming:</b> <ul style="list-style-type: none"><li>SystemVerilog (UVM), C++, C, Python, PERL, MatLab, TestStand, Tcl</li></ul>		
<b>Honors &amp; Awards</b>	Pegasus Gold Scholarship Recipient UCF President's List	Bright Future's Student	